FIG. 1

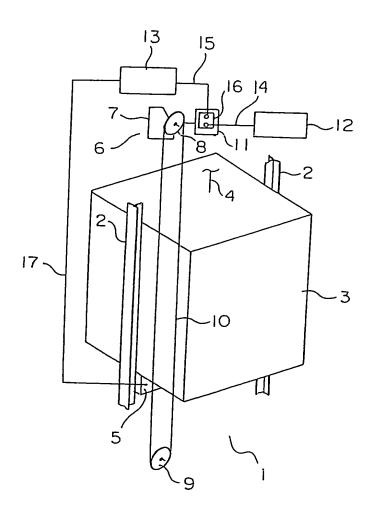


FIG. 2

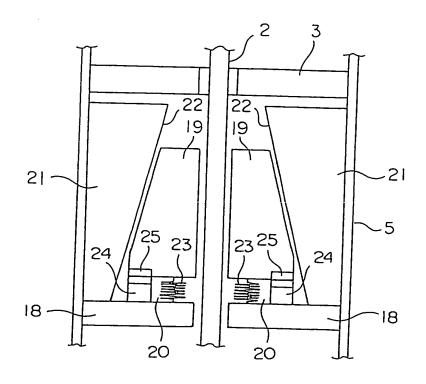


FIG. 3

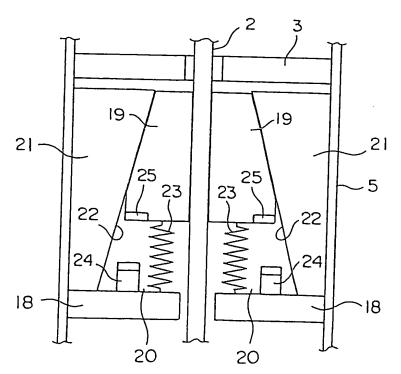


FIG. 4

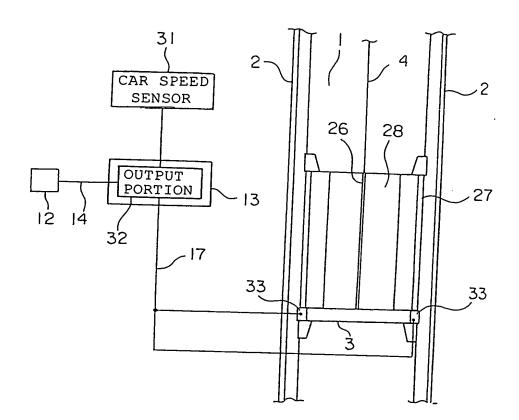


FIG. 5

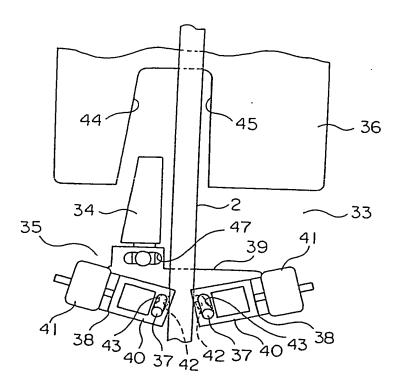


FIG. 6

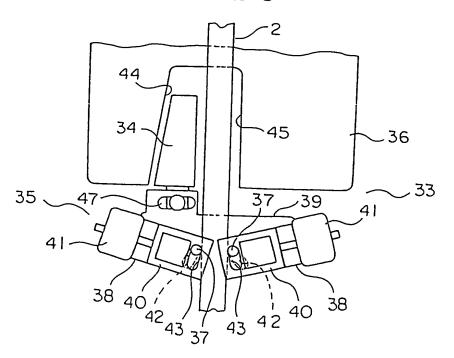


FIG. 7

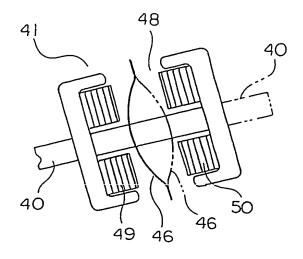


FIG. 8

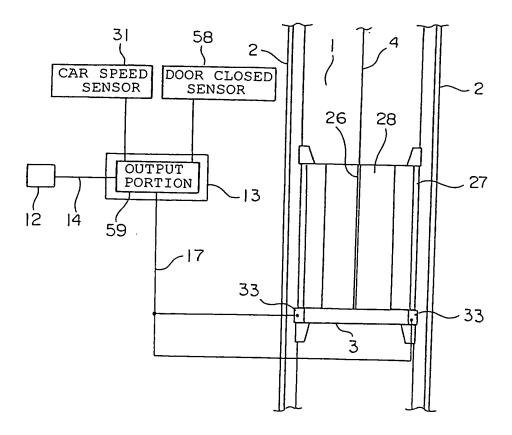


FIG. 9

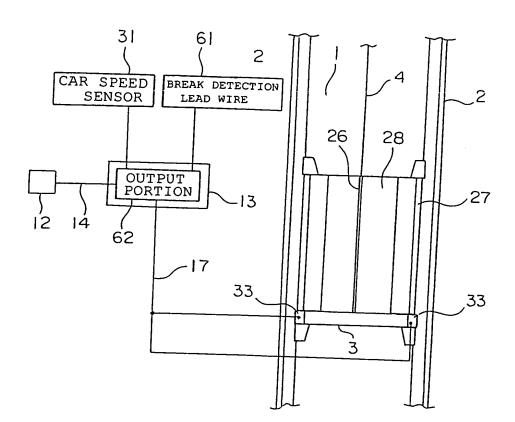


FIG. 10

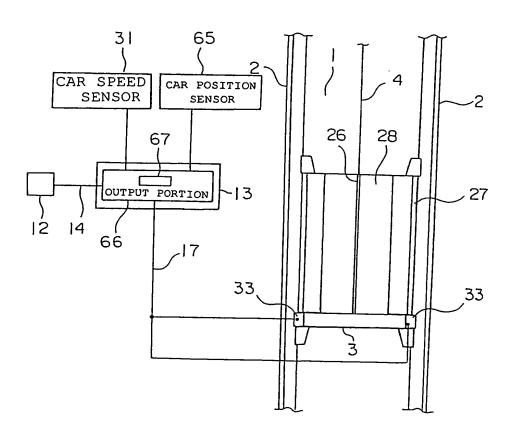


FIG. 11

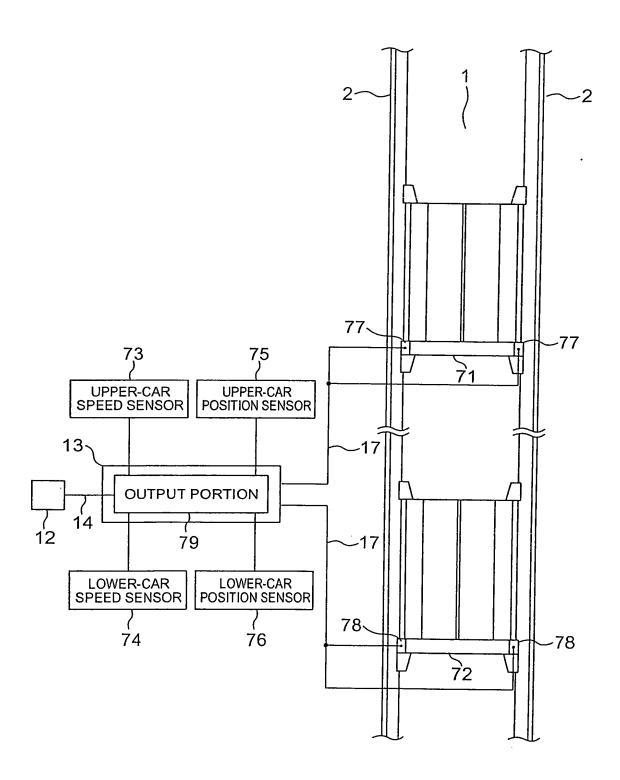


FIG. 12

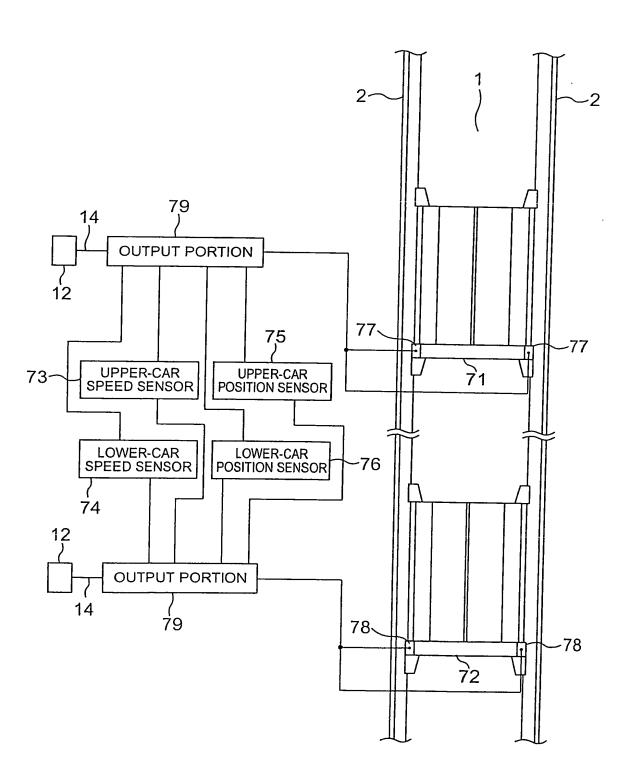


FIG. 13

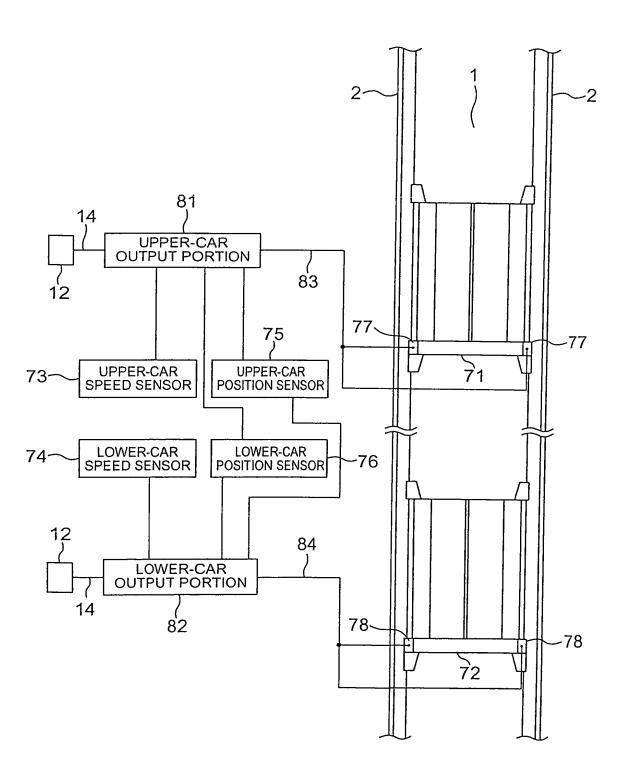
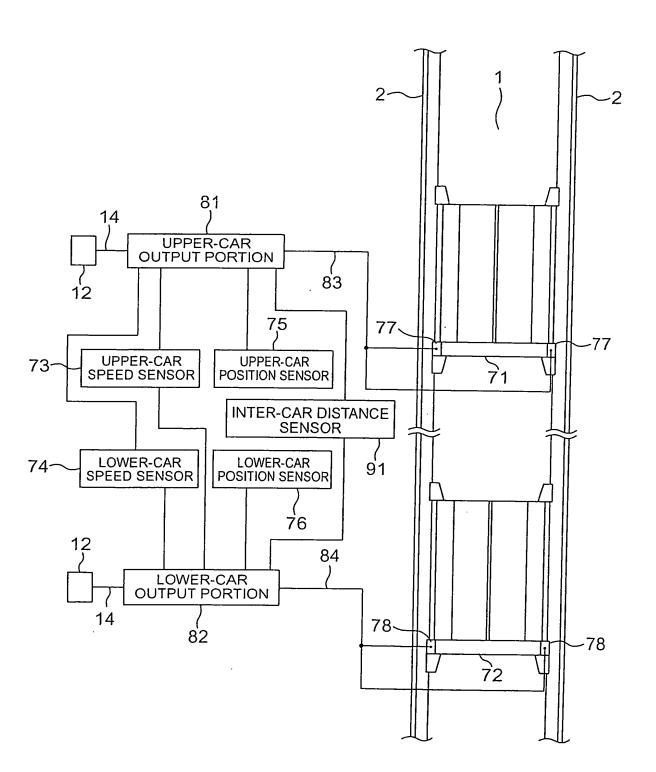


FIG. 14



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FIG. 15

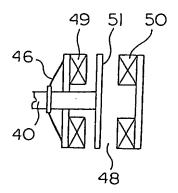


FIG. 16

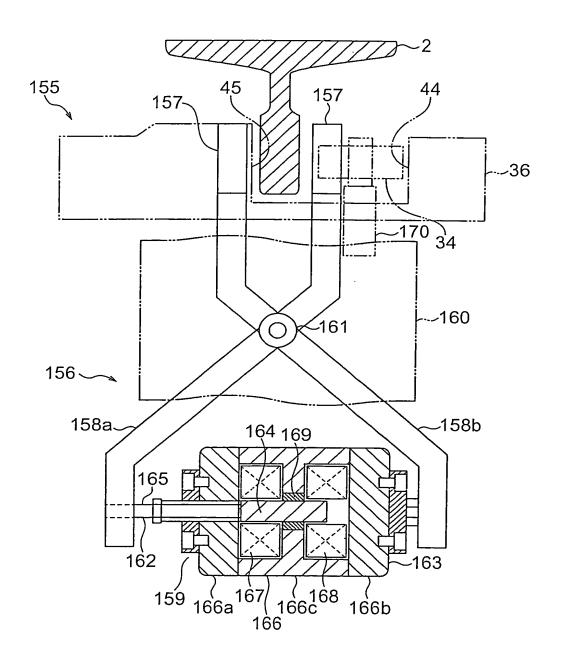


FIG. 17

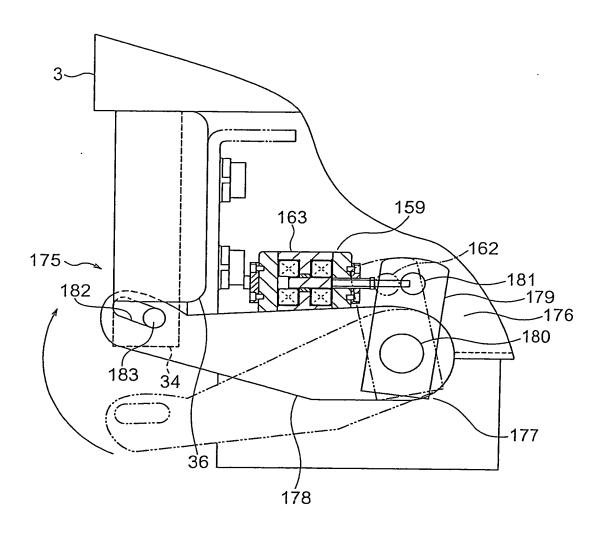


FIG. 18

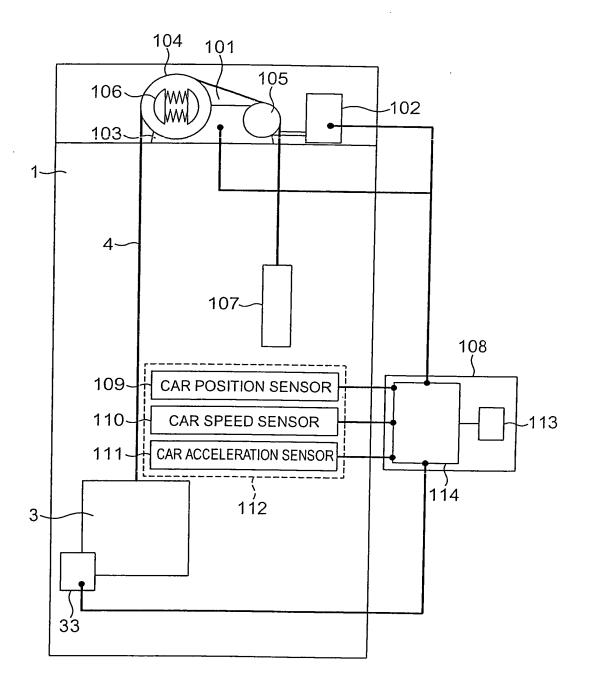


FIG. 19

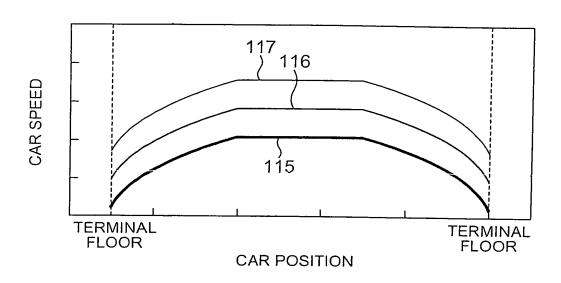


FIG. 20

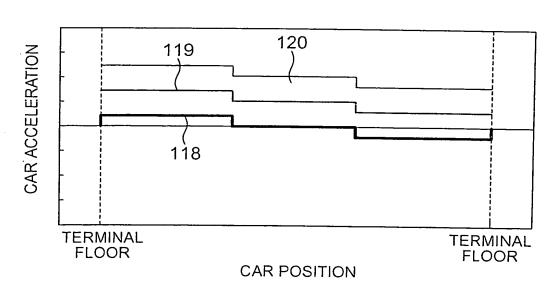


FIG. 21

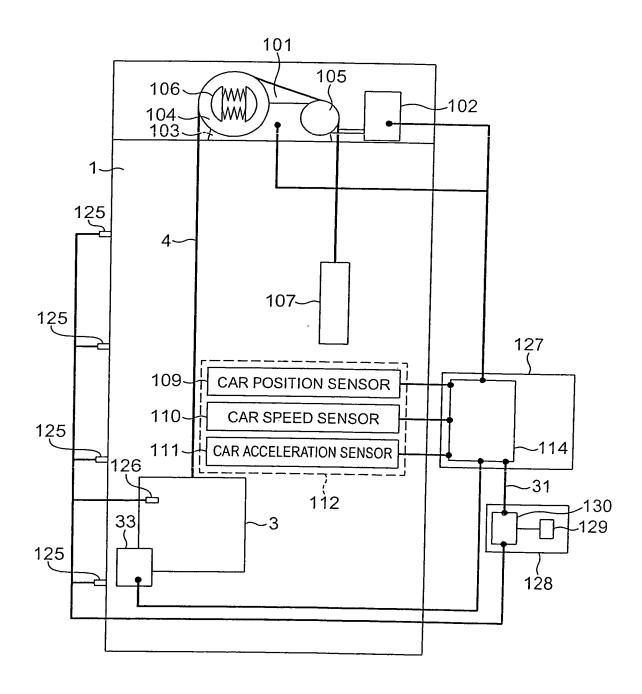


FIG. 22

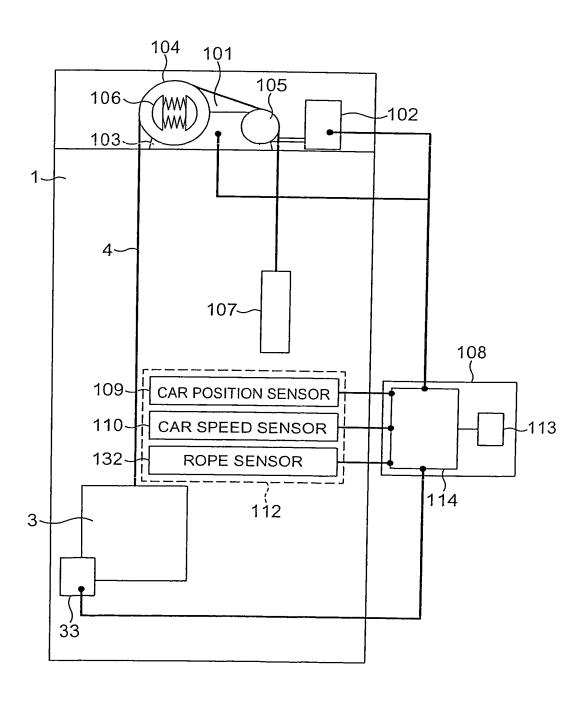


FIG. 23

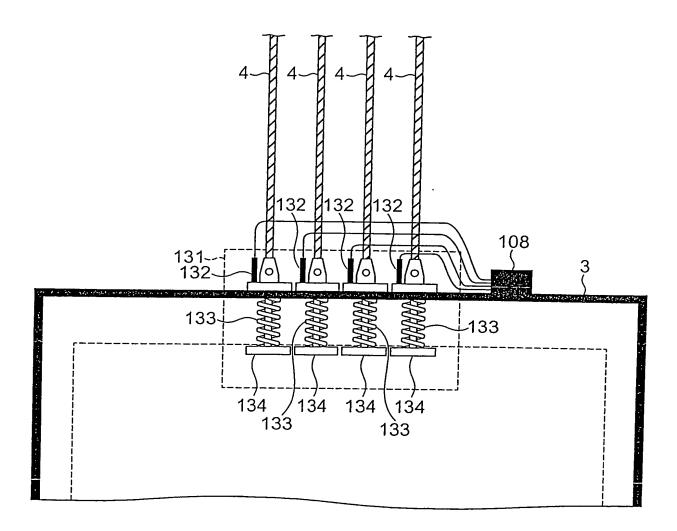


FIG. 24

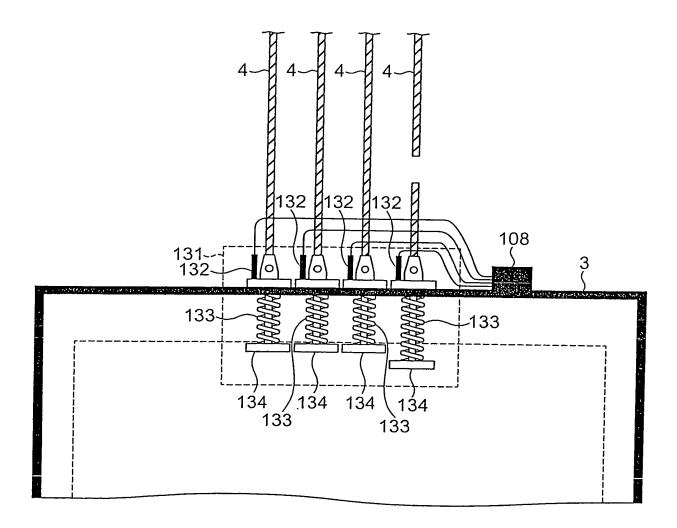


FIG. 25

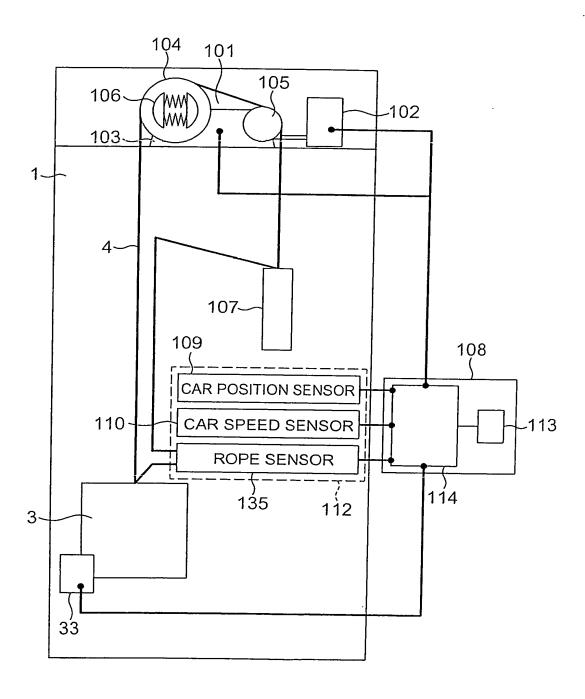


FIG. 26

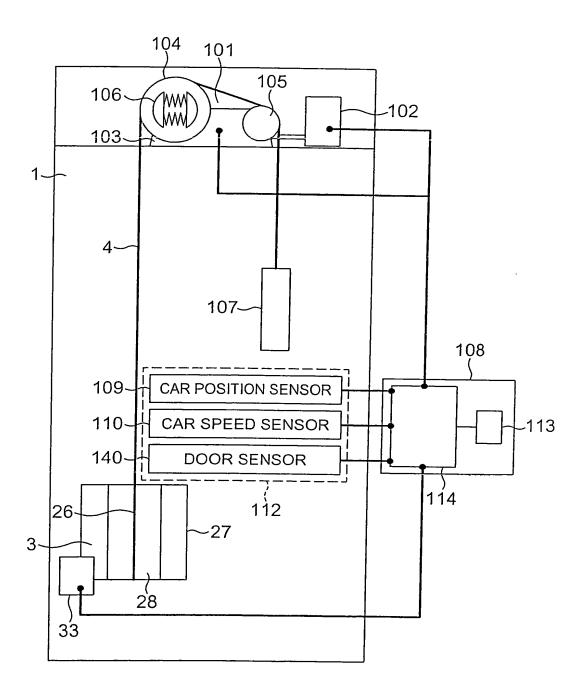


FIG. 27

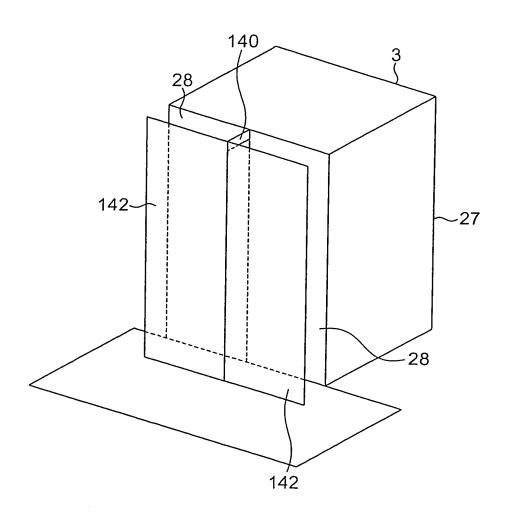


FIG. 28

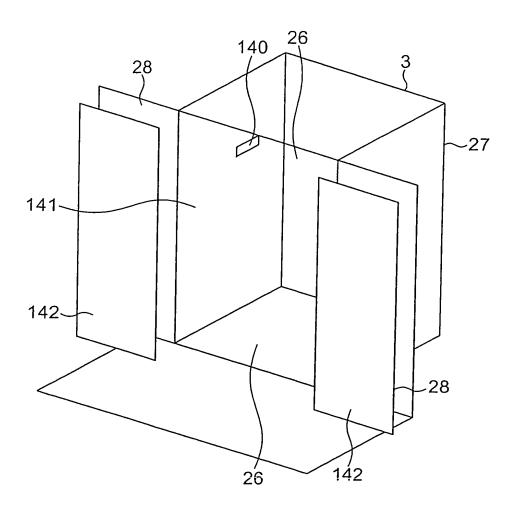


FIG. 29

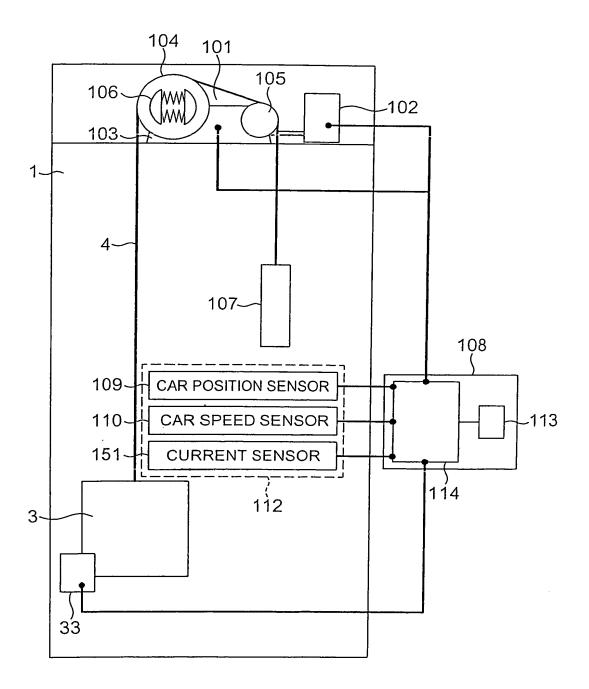
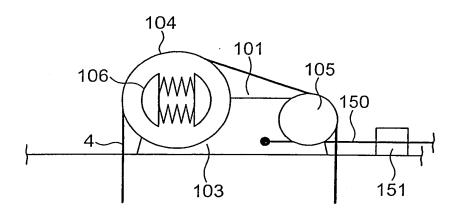
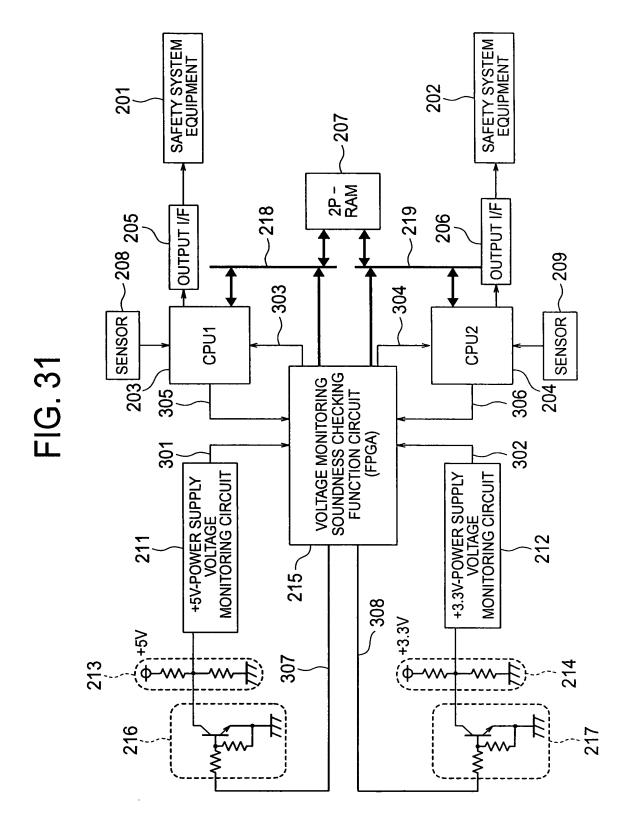
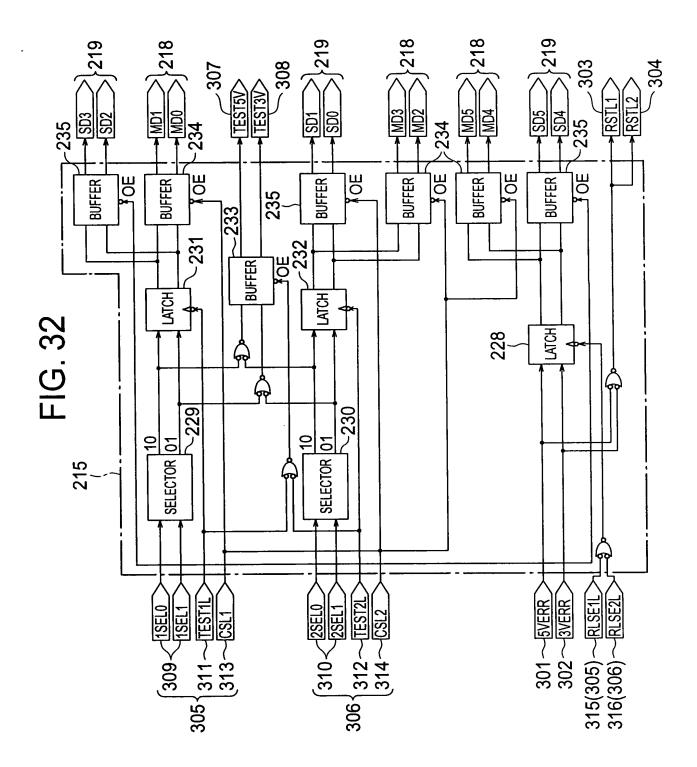


FIG. 30







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FIG. 33

DATA	MD5 (SD5)	MD4 (SD4)	MD3 (SD3)		MD2 (SD2)	MD1 (SD1)		MD0 (SD0)
5	<abnormal criterial="" value="" voltage=""> < SELECTED VOLTAGE VALUE FOR CHECKING SOUNDNESS (OF CPU ON THE OTHER SIDE)</abnormal>	CRITERIAL VALUE>	< SELECTED VC SOUNDNESS	LTAGE VALUE FO	SELECTED VOLTAGE VALUE FOR CHECKING SOUNDNESS (OF CPU ON THE OTHER SIDE) >	SELECTED VOLTAGE VALUE FOR CHECKING SOUNDNESS (OF CPU ON OWN SIDE) >	LTAGE VALUE FOR COPIC COPIC OF	OR CHECKING VN SIDE) >
MFANING	MD5(SD5) · 0→3.3V A	BNORMAL VOLTAGE	MD3 (SD3)	MD3 (SD3) MD2 (SD2)	SELECTED VOLTAGE	MD3 (SD3) MD2 (SD2)	AD2 (SD2)	SELECTED VOLTAGE
OF VALUE	OF VALUE 1→NORMAL VOLTAGE 1→NORMAL VOLTAGE NDA (SDA) : 0→57 ABNORMAL VOLTAGE	IAL VOLTAGE NORMAI VOITAGE	0+	← C	3.3V 5V	0 +	-0	3.3V 5V
	1→NORMAL V	IAL VOLTAGE	OTHER	OTHER THAN	NOT	OTHER THAN	THAN	NOT SELECTED
			ABOVE-IME COMBIN	COMBINATION	SELECTED	COMBINATION	ATION	

FIG. 34

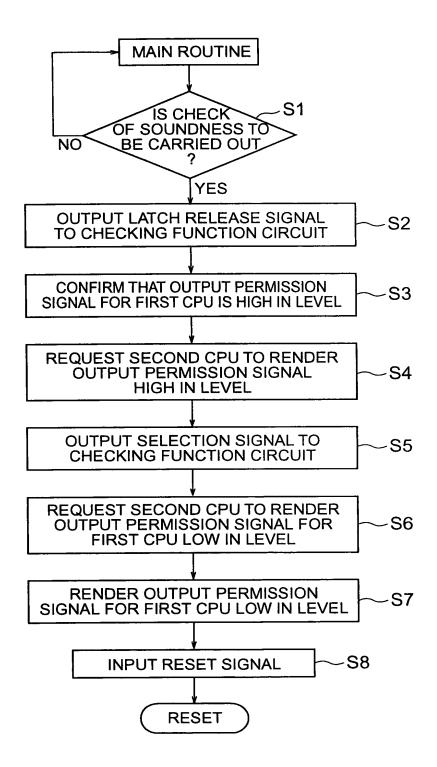


FIG. 35

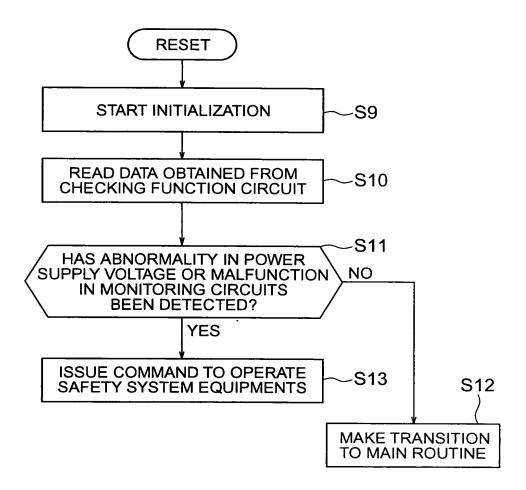


FIG. 36

